



500.40612X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Mitsugu KUSUNOKI

Serial No.: 09/944,134

Filed: September 4, 2001

Title: SEMICONDUCTOR INTEGRATED CIRCUIT WITH SUPPRESSED
CLOCK SKEW

September 22, 2005

**SELECTION OF PRACTITIONERS NAMED IN THE POWER OF ATTORNEY TO BE
RECOGNIZED BY THE OFFICE**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Pursuant to 37CFR 1.32(c)(3), the following practitioners are selected, from those named in the Power of Attorney for the above-identified application, to be recognized by the Office as being of record in the above-identified application:

<u>Attorney/Agent</u>	<u>Registration No.:</u>
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Respectfully submitted,

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